

6301, PROM 8C, 4 x 256 , Address Decoding Verification Table (by RCL9 - May 9 2026)

This table verifies the previously captured PROMS for D000, E000 and F800

	A5 (A15)	A4 (A14)	A3 (A13)	A2 (A12)	A1 (A11)	A0 (A10)	O4 /Select	O3 ROM IO	O2 RAM	O1 BD Select		
0							1	0	0	0	8	
1							1	0	0	0	8	
(etc. etc.)												
240	1	1	0	0	0	0	1	0	0	0	8	
241	1	1	0	0	0	1	1	0	0	0	8	
242	1	1	0	0	1	0	1	0	0	0	8	
243	1	1	0	0	1	1	1	0	0	0	8	
	(A15)	(A14)	(A13)	(A12)	(A11)	(A10)	/Select	ROM IO	RAM	BD Select		
243	1	1	0	1	0	0	0	1	0	1	0x05	0xD000 to 0xD3FF = Boot EPROM - Exidy Sorcerer
245	1	1	0	1	0	1	1	0	1	1	0x0B	0xD400 to 0xD7ff = Static RAM
246	1	1	0	1	1	0	1	0	0	0	8	
247	1	1	0	1	1	1	1	0	0	0	8	
248	1	1	1	0	0	0	1	0	0	0	8	0xE000 to 0xE3FF = Boot EPROM
249	1	1	1	0	0	1	1	0	0	0	8	0xE400 to 0xE7ff = Static RAM
250	1	1	1	0	1	0	1	0	0	0	8	
251	1	1	1	0	1	1	1	0	0	0	8	
252	1	1	1	1	0	0	1	0	0	0	8	
253	1	1	1	1	0	1	1	0	0	0	8	
243	1	1	1	1	1	0	1	0	0	0	8	0xF800 to 0xFAFF = Boot EPROM
255	1	1	1	1	1	1	1	0	0	0	8	0xFB00 to 0xFFFF = Static RAM